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Abstract: Nanotechnology is steadily transgressing from the laboratory to the commercial sphere and is enhancing products in a variety of sectors. Nanotechnology R&D has evolved from foundational discoveries aimed at understanding and exploiting nanoscale behaviour to an enabling technology. Nanomaterials are materials which are sized between 1 to 100 nm. Due to the basic characteristics of nanomaterials such as optical properties, reflection, transmission, absorption, and light emission, which are different from those of bulk materials, nanomaterials are useful in a variety of applications in different fields. In this paper the different types of nanomaterials have been outlined based upon their dimensions and applications in the field of electronics such as Quantum dots (QD's) in solar cells and Carbon Nanotubes and graphene in FETs.

Keywords: Quantum dots, carbon nanotubes, graphene, solar cells, FETs

I. INTRODUCTION

 ${f N}$ anomaterials are defined as "a natural, incidental or manufactured material containing articles, in anunbound state or as an aggregate or as an agglomerate and where, for 50% or more of the particles in the number size distribution, one or more external dimensions is in the size range 1 nm - 100 nm". This definition was adopted by the European Commission on 18 Oct 2011. The nanomaterials have optical properties like transmission, absorption, reflection and light emission, which are different from that of the bulk materials thereby rendering its significant usefulness in different fields. Nanomaterials are used in next generation electronic devices, high energy density batteries & magnetics, long lasting medical implants, ductile machinable ceramics & high-grade insulation, satellites & aerospace engineering components, clothing, cosmetics industry and clean energy technology amongst other areas.[91][92].

The dimensional classification is used to categorise nanomaterials as follows:

- 1) 0D materials where in all dimensions are in nanoscale 1-100nm.
- 2) 1D material wherein one dimension is outside nanoscale
- 3) 2D wherein two of the dimensions are outside anoscale and
- 4) 3D wherein the bulk nanomaterials are not in nanoscale in any dimensions

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Clusters Nanotubes, fibers and rods Films and coats Polycrystals OD 1D 2D 3D

Fig. 1 Classification based on dimensions.[115]

II. DIMENTIONAL

2.1. Quantum Dots

Quantum dots also called artificial atoms are zero-dimension particles in nanomaterials. These particles being in the nanometer range presents different electronic and optical properties as compared to their bulk material properties. The wavelength of light emitted by the quantum dots depends on their size wherein the blue and green colour of short wavelength and the red and orange colour of long wavelength are observed to be emitted by QDs of 2-3 Nm and 5-6 Nm respectively.

2.2. Synthesis Processes:

General techniques for quantum dot synthesis are top-down processing method and bottom-up approach.

2.3. Top-down processing methods:

As brought out by Valizadeh et al, "these include molecular beam epitaxy (MBE), ion implantation, e-beam lithography, and X-ray lithography. To achieve QDs of diameter approximately 30 nm, electron beam lithography, reactive-ion etching, and/or wet chemical etching are commonly used" [29,103].

QDs have also been fabricated using laser beams/ focused ion. However, QDs arrays fabricated through these processes have shown certain structural imperfections and impurity content [1, 103].

a. Bottom-up techniques:

The bottom-up technique includes the wet chemical and vapour phase methods for QD synthesis [1, 104].

As per Valizadeh et al [103], "wet-chemical methods are generally microemulsion, sol-gel, competitive reaction chemistry, hot-solution decomposition, sonic waves or microwaves, and electrochemistry".[2-4, 5-7,8] These processes are based on the method of precipitation for single/mixture of solutions under controlled parameters. The QD production in vapour phase method uses atom-by-atom growth of layers which is followed by self assembly of QDs on a substrate [9-12, 103]. As per Bera et. al., "self-assembly of nanostructures in material grown by MBE, sputtering liquid metal ion sources, or aggregation of gaseous monomers

are generally categorized under vapour-phase methods" [104].



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b. Hot injection method:

The hot injection is the predominantly used synthesis process for production of high quality QDs [106]. Several articles have been published reviewing the advantages of this process [13, 105]. However, a major disadvantage of this reaction method as observed in the reaction container of larger volume is the practical difficulty to achieve an instant homogeneous reaction.

c. Non injection methods:

It has been reported that using this method of synthesis nanocrystals of CdSe and CdTe of sizes within 5% standard deviation can be produced [14,15, 106]. This process of synthesis produces CdSe nanocrystals with zinc blende structure and a PL quantum yield of 30-40 % [14,15, 106]. By using processes similar to the non-injection approaches various families of II–VI binary and ternary magic-sized panocrystals (MSNs) have also been developed by Yu et al.

various families of II–VI binary and ternary magic-sized nanocrystals (MSNs) have also been developed by Yu et.al [16-22, 106]. The core-shell configuration and single sized nature of the magic-sized nanocrystals are thermodynamically better with extremely narrow absorption/emission width making it an attractive choice for applications like solid-state lighting and telecommunications [106].

Non-injection synthesis method has also been used to produce ternary systems like CdTeSe magic-sized nanocrystals and CdSeS regular nanocrystals [106].

d. Solvo thermal method or hydrothermal method (in aqueous medium)

Though this method has been used to synthesise semiconductor quantum dots this has been largely restricted by the inferior product properties as per Hu et. al [106].

e. Microwave and flow through processes:

This may be a possible approach towards large scale synthesis of semiconductor nanocrystal quantum dots as it features simple operation [23]. Producibility of high-quality nanocrystal of the colloidal semiconductor variety is possible using microwave synthesis in organic and aqueous media [106].

2.4. Applications of QDs:

a. Solar Cells:

Burnham and Duggan were the pioneers to conceive the use of QDs for high efficiency solar cells in 1990. A large variety of solar cells have been developed over the past few decades. The high-performance solar cells of crystalline silicon and GaAs materials and the thin film solar cells made from CdTe and CIGS materials are constrained by the higher material and fabrication costs while Cadmium and telluride are rare and highly toxic metals [106]. On the other hand, dye-sensitized and organic solar cells have the advantage of higher flexibility and lower production cost.

Schaller et. al., in 2004, reported "spectroscopic evidence that several excitons could be efficiently generated upon absorption of a single energetic photon in a quantum dot, an approach now known as CM (carrier multiplication) or MEG (multiple exciton generation)" [107]. "This property boosts the energy conversion efficiency beyond the traditional Shockley and Queisser limit for silicon solar cells [24] through increased photocurrent. LANL's dots were made from PbSe." As per Hu et. al., "three types of solar cells have been reported in this category [106]:

Retrieval Number: 100.1/ijaent.D0464084621 DOI:10.35940/ijaent.D0464.094621 Journal Website: www.ijaent.org i)Quantum dot/metal Schottky junction solar cell ii) Polymer/quantum dot hybrid solar cell

iii)Quantum dot/ quantum dot D/A solar cell"

Hu et al has brought out that many QD solar cells with Schottky junction structure have been reported based on PbS [25,26], PbSe [27], and PbS_xSe_{1-x} [28] nanocrystal systems which hold the capacity to utilize the infrared part of the solar energy because of the small bandgap of the nanocrystals which leads to a power conversion efficiencies of 2.1 % for PbSe [27], 2 % for PbS [26], 3.3 % for PbS_xSe_{1-x} based devices and monochromatic power conversion efficiency (MPCE) of 4.2 % [28]. Various processes like the layer-bylayer dip coating [26,27] and spin coating [25] are employed for fabrication of the nanocrystal layer using P3HT / MEHPPV as the active polymer mixture layer to enhance electron transport, light harvesting and performance [106]. In order to ensure that the exciton reach the donor and acceptor, the domain lengths and exciton diffusion length should be of comparable magnitude. A higher power conversion efficiency has been reported by using a combination of CdSe nanorod and P3HT.

Polymer/organic materials have problems of stability which can be overcome by developing pure inorganic QDSC. Alivisatos worked using a "CdSe/CdTe nanorod system and a bilayer heterojunction structure, devoid of organic materials, reporting improved atmospheric stability and 2.9 % power conversion efficiency"[30].

It has also been observed that sensitisation of solar cells using QDs gives improved power conversion efficiency as reported in the case of "TiO₂ solar cell sensitized with CdSe with a cobalt (II/III) based redox system" [31,32].

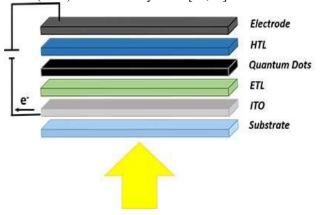


Fig 2: Arrangement of QD Solar Cell taken from [48]

Advantages of using QDs in solar application: QDs combines the benefits of inorganic and organic materials. Devices incorporating QDs have the following advantages:

- (i) low cost
- (ii) solution-based
- (iii) enhanced charge separation efficiency
- (iv) higher PL quantum efficiency
- (v) better chemical stability
- (vi) covers infrared spectrum
- (vii) compatible to flexible and large area substrates
- (viii) thin layer to achieve complete absorption





In 2014, according to a research group at MIT and a thesis published by Chuang, an air-stable, room temperature, solution processed ZnO/PbS solar cell was demonstrated which reached a certified efficiency of 8.55%, which broke previous record efficiencies of QD solar cells. Photoelectronic spectroscopy confirmed that this improvement was due to the relative band alignment between OD layers. Apart from enhanced efficiency, these cells demonstrate excellent longterm stability in air for more than 150 days without encapsulation. The quantum dots in this architecture serves complimentary functions of light absorption and electron blockage/hole extraction. An important factor was the choice of ligands- Iodide-passivated QDs were found to be stable in air, while organic ligand passivated QDs were found to be prone to oxidation. Another important factor was the removing the commonly used MoO3 interfacial layer, which gradually develops an unfavourable band alignment in the device with air exposure time.

Around 2016, the first colloidal QD solar cell was built by a research team at the University of Toronto led by Edward H Sargent of University of Toronto with power conversion efficiency of almost 11.3%.

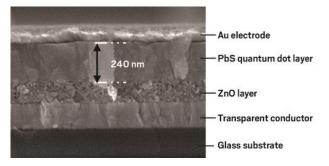


Fig 3: SEM image shows how record setting quantum dot solar cells are built [87]

In 2017, a colloidal QD solar cell was created by the scientists at National Renewable Energy Laboratory (NREL) which achieved a record 13.4% conversion efficiency.

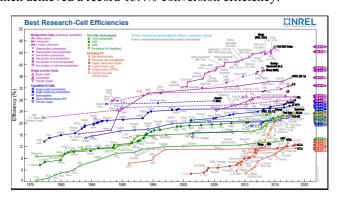


Fig 4: Growth curve of efficiencies of QD solar cells through the years shown in red hollow diamond key as noted by NREL [48]

a. Displays and Solid-state lighting:

The higher chemical stability which enables purer colour emission, lower fabrication costs and compatibility to fexible large substrates make the use of quantum dots more desirable. Multiple colour emission can be achieved using the sizetuneable emission property. The limitations like ageing and low intermixture stability associated with tuneability using

Retrieval Number: 100.1/ijaent.D0464084621 DOI:10.35940/ijaent.D0464.094621 Journal Website: www.ijaent.org multiple compositions are therefore eliminated. Moreover, a high colour rending index (CRI) value is achieved due to the continuous tuneable QD.

b. Light Emitting Diodes (LEDs):

The salient features required for LED are tuneable and narrow band emission. Both of these are intrinsic properties of QDs basing on their size and composition. Moreover, the printability of QD-LEDs, especially on plastic opens up a host of application for opto-electronic devices which requires flexibility over large areas and likely to be adopted for visual output devices in future.

As per Lutfullin et.al., Figure 5 depicts the outline arrangement of a QD-LED. The outline process proposed by Lutfullin et.al., is as follows [48]:

- (i) Spin coating an HTL (hole transporting layer) on ITO (indium tin oxide) and attached to a glass / polymer substrate.
- (ii) Deposit QD's by spin-coating. (Perovskite, CdSe or InP based QDs are normally utilized for visible LEDs and PbS QDs are used for infrared LEDs)
- (iii) Thereafter the electron transporting layer (ETL) and electrodes are deposited. (Electrodes are of metals like Ag, Au, and Al and are deposited using a thermal evaporation system [35-40]

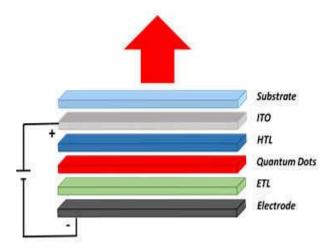


Fig 5: Arrangement of QD LED. Taken from [48]

Some of the salient developments on LED reported by Hu et.al., is summarised below [106].

- (i) QD-LED works on the principle of electroluminescence as against conventional LED which works on photoluminescence.
- (ii) QD-LED provides more freedom in device design compared to photoluminescence LED which require a pumping source.
- (iii) QD electroluminescence is achieved through the Förster resonance energy transfer (FRET) or direct charge injection methods [106].
- (iv) A potential phosphor for white Light Emission is MSQD using CdSe.
- (v) In 1994 Alivisatos group produced the first QD-LED [108].

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- (vi) Klimov et al. reported "55% light conversion efficiency, in which a monolayer of CdSe/ZnS quantum dots was put on top of the InGaN quantum well" [33].
- (vii) Sun et al., in 2007 reported "QD-LED brightness values of 9064, 3200, 4470 and 3700 cd/m² for red, orange, yellow, and green QD-LEDs respectively" [34].
- (viii) The smaller QD size and low human optical sensitivity offers a challenge for blue light. Tan et al reported the best QD-LED with CdS/ZnS core/shell and omitted ETL to remove extra organic emission [109].
- (ix) White QD-LED not yet commercialised to replace white LED using QD as phosphor.
- (x) "Red, green and blue QD-LEDs were successfully fabricated by patterning QDs of different sizes on a layer of zinc oxide nanoparticles in 2012 with brightness of 23,000 cd/m^2 and 218,000 cd/m^2 for red and green respectively" [88].
- (xi) Inorganic QD-LED improves material degradation and electrode contact and offers increased life [110].

d. Phosphors:

White lights or horticultural lights (**Fig 6**) can use QDs as phosphors for conversion of blue light to white light with high CRI (colour rendering index) and CCT (correlated colour temperature). A mix of red and green phosphors placed either inside or outside the blue LED package to produce white light are defined as 'on-chip' or 'remotephospor' type respectively. Different blends of phosphors are used for producing various colours of light. For example, red QD phosphors on a blue LED produces a combination of red and blue light which is used in horticulture.

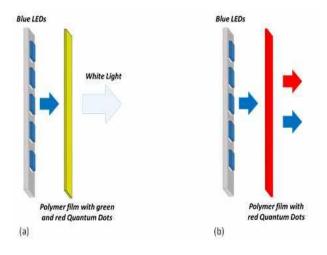
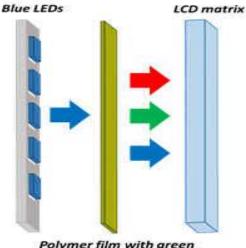


Fig 6: "Schematics of QD based device for (a) white lighting and (b) horticultural lighting", taken from [48]

e. Liquid crystal display (LCD) backlighting:

The narrow FWHM of light produced using QD is used effectively in the backlighting of the LCD wherein a combination of blue LED and a QD filter with red and green quantum dots is used. The QD filter converts part of the blue light to red and green lights. These lights are thereafter combined to produce a wide range of colour lights on the display. The 'on-chip', 'on-edge' and 'on-surface' are the three types of arrangements used. The 'on-surface' configuration is shown in **Fig 7**. For usage in LCD backlighting, Perovskite QDs (CdSe-based and InP-based) are very effective. [41-45]

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Polymer film with green and red Quantum Dots

Fig 7: Schematics of Quantum Dots LCD backlighting in the 'on-surface' configuration taken from [48]

f. Photodetectors:

In view of the simple processes involved in combining QDs with silicon based electronic components and their influence on optical emission/absorption behaviour based upon the size and type, QDs have found extensive use in photodetectors and image sensors. These applications cover imaging devices including visible light and night vision cameras, gas detection spectroscopy devices, machine vision and other surveillance devices.

Fig 8 shows the structure of a QD based photodetector. A photodetector is fabricated by depositing electrodes on a glass/ ceramic substrate by evaporation. Thereafter, spin coating of colloidal QDs is done on the substrate forming a solid QD film/ composite between electrodes [46-47]. Infrared photodetectors normally use PbS QDs and UV detectors use perovskites (CdSe and InP based) QDs.

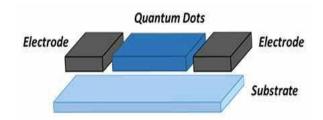


Fig 8. Arrangement of a QD photodetector taken from [48]

III. DIMENTIONAL

3.1. Synthesis Processes:

Arc Discharge Method

Though a common method for CNT production, this technique requires the separation of CNTs from soot and catalytic metals.[53]



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3.2. Carbon Nanotube

A type of arc method synthesis called Hipco method is achieved under high pressure and was established at Rice University to produce high quality SWCNTs from the gasphase reaction of iron carbonyl with high pressure carbon dioxide gas. Synthesis using this method creates high quality materials in small quantities and not feasible for commercial distribution.

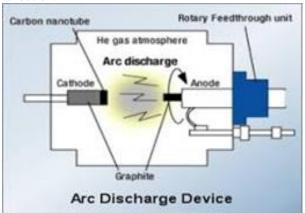


Fig 9: Arc Method for CNT Synthesis[114]

a. Laser methods

Initially a dual pulsed method was used to synthesize carbon nanotubes with >70% purity. The process uses two consecutive laser pulses to reduce the carbon collected as soot and further breaks up the higher sized particles and adds them to the developing nanotube structure. The product looks like a mat of "ropes", whose diameter is about 10 to 20 nanometres and have a length of approximately $100\mu m$. in every rope we can find a bundle of SWCNTs which lie parallelly to a common axis. The nanotube diameter and alignment can be changed by varying the different parameters including catalyst configuration and temperature.

b. Chemical Vapor Deposition

Catalysed chemical vapor deposition of hydrocarbons is commonly used for CNT synthesis. Catalytic Chemical Vapour Deposition of C2H2 over cobalt & iron catalysts supported on silica or zeolite produces large amounts of CNTs. The catalyst's cobalt content influences the carbon deposition and pH affects the CNTs' selectivity.

It can be shown experimentally that the growth of SWNT or bundles of SWNT in ethylene atmosphere can be induced by supported catalysts (eg. Fe,Co,Ni) which contains either a single metal or a mixture of metals. Carbon nanostructures with open ends and no caps have also been prepared by using ethylene with reaction temperatures of 545°C for Nickel-catalysed CVD, and 900°C for an uncatalysed process. SWCNT and DWCNTs on Mo and Mo-Fe alloy catalyst can also be produced.

Nanotube chips which have isolated SWCNTs at controlled locations have been produced using CH4 as a carbon source. High yields of SWCNTs can be formed by catalytically decompositing a mixture hydrogen and methane over well dispersed metal particles (Co,Ni,Fe) on MgO at 1000°C. In this process the CH4 environment which decomposes at high temperature stops further growth of the nanotubes resulting in a higher ratio of SWCNT.

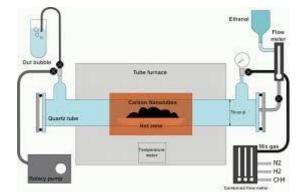


Fig 10: Catalyzed CVD for CNT Synthesis [114]

Plasma arcing of carbonaceous materials, specifically graphite can be used to create CNTs and fullerenes.

The other methods of producing CNTs are diffusion flame synthesis, electrolysis, polymer heat treatment and lowtemperature solid pyrolysis.

3.3. Applications of CNTs

The SWCNT presents excellent structural and conductive material properties. Each atom donates a delocalized pi electron which is not bound by the donor atom and has the freedom to oscillate throughout the structure. This resulted in "the first known molecule with metallic-type electrical conductivity". The high-frequency C-C bond vibrations in carbon nanotubes provides intrinsic thermal conductivity which is even higher than diamond. Thus, extraordinary electrical and heat conductivity and mechanical properties can be seen. They are excellent electron field-emitters. Keeping all these properties in mind plus the fact that CNTs are carbon polymers offer opportunities for further modifications of structure and optimization of dispersion as well as solubility. CNT's can be called "molecularly perfect, which means that they are normally free of property-degrading flaws in the nanotube structure".

a. CNT Field Effect Transistor

Scaling the silicon MOSFET as per Dennard scaling was to shrink the MOSFET, reduce the voltage and maintain a constant E-field which would result in increased current density, lower power and faster switching. [56] The Dennard transistor scaling prediction [89] is depicted in Fig 11.

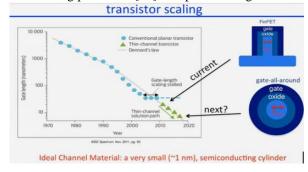


Fig 11: Transistor scaling [89]

It was apparent that this level of channel scaling in MOSFET was inhibitive.

On the other hand CNTFET with a typical arrangement as shown in Fig 12 showed no degradation down to channel lengths of 9 nm and transistor performance in terms of current density was 3 times higher than silicon as reported by Franklin et al (Fig 13a). As brought out by Tulevski et al in 2014 the CNT based FET performance is three times higher while the power input is three times lower as compared to FinFET as shown in Fig 13b. Kim et al., has reported that in case of MOSFETs and CNTFETs of same channel length, Power Delay Product (PDP) of MOSFET based logic gates is 90 to 100 times greater than the PDP of CNTFET based logic gates.[93][94].

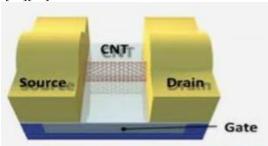


Fig 12: CNTFET[111]

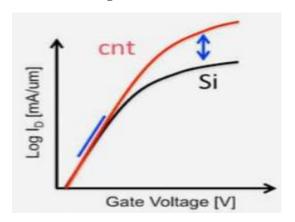


Fig 13a: Current Density comparison

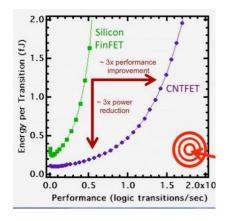


Fig 13b: Performance & Power Comparison

Hence the CNT-FETs were proposed as the future choice which uses the semiconductor CNTs as the channel. The physical structure of CNTFETs is very similar to that of MOSFETs. CNTFET have high drive current, large transconductance, high temperature resilience, strong covalent bond. It can also reduce the scattering probability due to the 1D confinement so that the device may operate in

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ballistic regime has brought out by Franklin et al in 2012. On the basis of geometry, CNTFETs can be classified as:

(i) Bottom Gate CNTFET:

Bottom gate CNTFETs require very high gate voltages to be turned on. Apart from this, they exhibit low transconductance, high on-state resistance of several MQ, and no current saturation

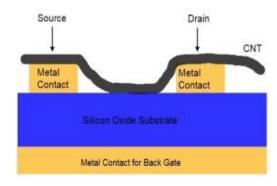


Fig 14: Bottom Gate CNTFET - side view of carbon nanotubes deposited on a silicon oxide substrate [111]

(ii) Top Gate CNTFET:

Top gate CNTFETs improves the device performance. A better turnout is obtained by this structure due to the adoption of better device geometry and scaling of dimension [111]

Gate dielectric

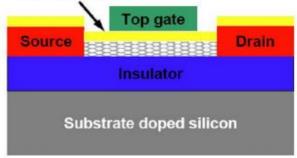


Fig 15: Structure of Top Gate CNTFET [111]

(iii)Coaxial gate CNTFET

Coaxial gate CNTFET is also known as gate all-around CNTFETs. It improves the electrical performance of the CNTFET, improving the device on/off ratio and reduces the leakage current.

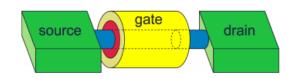


Fig 16: Structure of coaxial gate CNTFET [111]

On the basis of operation, CNTFETs can be classified as follows:



12



(i) Schottky barrier CNTFET:

The working of SB-CNTFET can be explained by the principle of direct tunnelling through the Schottky barrier at the source channel junction. The gate voltage controls the barrier width and hence the transconductance. There is limiting of the channel current due to the large barrier present as a result of low gate bias. The barrier width reduces with increase in the gate bias which further increases the quantum mechanical tunnelling through the barrier and eventually the current flow in the transistor channel. The modulation of the transmission coefficient is responsible for the transistor action.

Schottky-Barrier CNTFET

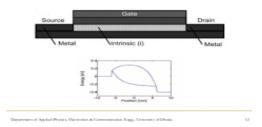


Fig 17: Schottky barrier CNTFET [111]

(ii) MOSFET like CNTFET:

These structures use heavily doped terminals as against the metal terminals in Schottky barrier CNTFET. It overcomes problems faced by SB-CNTFETs by operating like normal MOSFETs. It works "on the principle of modulation of the barrier height by gate voltage application. The drain current is controlled by the charge induced in the channel by gate terminal".[95].

Although carbon nanotube (CNT) is accepted as the best replacement for silicon in field-effect transistors. [51,54], the evolution of the industrial production process holds the key. Despite IBM researcher's ability to create a logic circuit with 10,000 CNTFETs on a single chip based on "high density integration of carbon nanotubes via chemical self-assembly" [50,51,52,96,97,101] and the Stanford engineers' making of the first nanotube computer which is equivalent to Intel 4004, the replacement of silicon with CNT around 2020 holds a major challenge. As per the semiconductor industry roadmap, by 2020 "transistors must have features as small as 5 nm to keep up with the continuous miniaturization of computer chips. The current best is 14 nm".

The challenges facing CNT is the difficulty in obtaining the high purity of semiconductor CNTs and placing them correctly. The CNT available commercially contain about 33-60% metallic CNTs which are prone to initiating failures in FET by short circuiting. In the past, manufacturers achieved 88-94% precision which is much below the estimated 99.96% specified by ITRS for commercial manufacturing. Arrays of CNTs with very high purity have to be built which is a challenge faced by the industries. Chen et al., and Cui et al., proposed plasma treatment to convert metallic CNT to semiconductor type and Na et al., proposed protein-coated nanoparticles in the contact areas to convert metallic CNT to semiconductor type to address the purity aspect. With regard to building arrays of high purity CNT the engineers of Wisconcin University have reported a technique called floating evaporative self-assembly.

Retrieval Number: 100.1/ijaent.D0464084621 DOI:10.35940/ijaent.D0464.094621 Journal Website: www.ijaent.org Claims of achieving purity rates of 99.98% have been made by the Wisconsin team. The "schematic illustration of the iterative process used to fabricate aligned nanotubes-SWCNT" driven by the spreading and evaporation of controlled doses of organic solvent at the air water interface is given in the figure below:

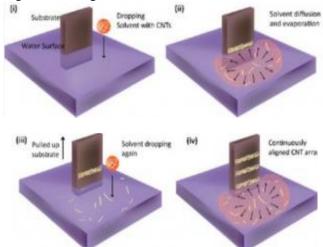


Fig 18: Fabrication of aligned nanotubes [112]

CNTs are outstanding materials for super capacitor electrodes. The incredibly high electrical conductivity and sharpness of the tip of CNTs are responsible for the claim that these are the best-known field emitters of any material. As we reduce the tip's radius of curvature, the concentration of the electric field increases which further increases the field emission. The sharpness of the tip also means that the emission occurs at low voltages which is an important feature used in the construction of low-power electrical devices. CNTs also have the ability to carry a significant amount of high current density". Towards this, CNTs have a variety of applications which include field emission flat panel displays using separate nanotube electron gun for each individual pixel in the display as against the older CRT display with single electron guns. Using these characteristics, CNTs have other applications as well, such as- general types of low-voltage cold-cathode lighting sources, lightning arrestors, and electron microscope sources.

3.4. Carbon Nanofibre

3.4.1 Synthesis Processes:

There are a number of techniques used to fabricate nanofibers, some of them are mentioned

a. Drawing:

Drawing is the technique to fabricate long single nanofiber one-by-one from the droplet of polymer. The diameter of the resultant nanofiber depends on the type of the polymer, its composition, drawing velocity and speed of evaporation of the solvent. Only a strong viscoelastic material that can undergo strong deformation during this process when stress is applied while pulling the nanofibere, can be used in this process. This is a major practical disadvantage. Thus, choice of material is limited in this process.



b. Template Synthesis:

A metal oxide membrane having pores of nanoscale diameter is used in this process. The metal oxide membrane is placed over a solidifying solution. Polymer solution is extruded by the membrane by applying high water pressure over it. The polymer solution after passing through the membrane comes in contact with solidifying solution which converts the polymer solution into nanofibers. The diameter of the nanofibers depends on the diameter of the pores of membrane.

c. Phase Separation:

In this mechanism, separation of phases is involved due to the physical incompatibility. A polymer is mixed with a suitable solvent, gelation occurs in this mixture and the final step involves the separation of phases. One of the phases-which is that of the solvent- is extracted leaving behind the other remaining phase. The remaining phase is the nanofibrous structure.

d. Self-Assembly:

Self-assembly processing involves the smaller molecules as basic building blocks to build-up the nanofibers. Molecules are spontaneously organized into an individual and stable structure with pre-programmed noncovalent bonds. Nanofibers of very thin diameter can be fabricated using this process, but it requires very complicated procedures. The low productivity is another limitation of this method.

e. Electrospinning:

Electrospinning involves the application of high voltage on melts or solutions which results in the formation of a pendant droplet. The pendant droplet deforms into a conical droplet or taylor droplet at the tip of the needle when the surface tension of the fluid is overcome by the increasing electrostatic repulsion. With further increase in voltage when the surface tension of the taylor droplet is overcome, a "fine charged jet of polymer solution is ejected from the needle tip". [113] This interaction between the electric field and the surface tension of the fluid leads to stretching of the jet stream making it undergo a whipping motion leading to the evaporation of the solvent. The jet stream transforms into a long thin filament which eventually solidifies and deposits on a grounded collector and forms a uniform fibre. This process produces fibres with a range of diameters from nm to µm scale. Electrospinning is regarded as a versatile and simple method to generate ultrafine fibres that are between tens of nanometers and several micrometers in diameter using materials such as inorganic materials, polymers, composites etc. Electrospinning is a continuous process that results in the formation of longer fibres compared to fibres prepared by other chemical or physical methods [57]. Tuneable morphologies and structures can produce desired properties by controlling the process parameters and/or designing special electrospinning apparatus. A unique advantage of electrospinning is that complex hierarchical structures can be obtained via controlled calcinations which is not easy using conventional methods. [58].

There are some disadvantages of this method. Firstly, in the preparation of organic NFs, the variety of polymers used in electrospinning is limited and the structure and performance of NFs are not well researched. Secondly, the performance and range of application of electrospun inorganic NFs have

been limited due to their friability after calcination, although inorganic NFs have a potential application in many fields such as energy devices, high temperature filtration, biological tissue engineering, and efficient catalysis. Thirdly, electrospinning has been implemented at industrial level; however, in terms of producing fibres for the application of filters electrospinning is inferior to traditional methods due to its higher cost to produce fibres with large diameter. Furthermore, it remains a challenge to fabricate NFs with diameters less than 10 nm by electrospinning [59].

3.5. Applications

a. Dye sensitized Solar cells:

Dye-sensitized solar cells (DSSCs) can directly convert light into electricity with the help of a photosensitizing dye. A DSSC has three primary components: a photoanode, a counter electrode, and an electrolyte.

The composition of photoanode is a coating of porous semiconductor film on a transparent conducting glass where the photosensitizing dye is adsorbed on this porous film. In DSSCs, once the photosensitizer absorbs photons, the photoelectrons first move into the conducting band of the semiconductor from the photosensitizer and are collected on the photoanode. The photoelectrons further transfer to the counter electrode through the external circuit to form current. In this process, the maximum light absorption and efficient charge transport affect the overall photoelectric conversion efficiency. Hence, the photoanode plays a vital role. Electrospun metal oxide nanofibers as a thin film coated with photoanode has been thoroughly researched due to their high specific surface area and 1D fibrous morphology. The high specific surface area enhances the absorption of photosensitizing dye and the 1D fibrous morphology results in a better charge conduction capability and reduced chargecarrier recombination [60,61]. In addition, the large and controllable pore sizes of electrospun NFs contribute to the penetration of viscous polymer gel electrolyte [62]. TiO₂ anatase NFs prepared by electrospinning have been mainly used as photoanode material [63,64,65]; however, one of the major problems is their poor adhesion to conductive substrate. Many effective methods have been adopted to solve the problem, such as converting electrospun nanofibers into nanorods [66], using hot press pre-treatment [67], and introducing ultrathin surface treatment layer (STL) [68].

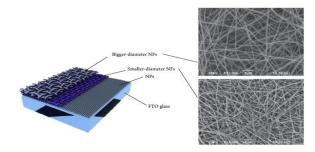


Fig 19: SEM images of the bilayer TiO2 nanofibers photoanode of a dye-sensitized solar cell (adapted from [74])

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IV. DIMENTIONAL

a. Graphene

Graphene consists of single-atom thick sheets of sp2 bonded carbon atoms arranged in a 2D honeycomb lattice. "Because of this structure, graphene is characterized by a number of unique and exceptional structural, optical, and electronic properties". [69,90] It has increased strength due to its covalent C-C bonds which can be used in mechanical applications. It can improve the thermal conductivity of materials resulting in more efficient heat dissipation of electronic components. The impervious property of graphene to both gases and liquids makes it an appropriate material to be exploited for a wide range of barrier applications. It is highly suitable to be used in energy storage applications due to its exceptional ability to store electrical energy at very high densities.

4.1.1 Synthesis processes:

A number of methods can be used to produce graphene sheets; however, the three primary methods are Mechanical Exfoliation, Chemical Vapor Deposition (CVD) onto metal or Si substrates, and Chemical, Electrochemical, or Thermal Reduction of GO.

a. Chemical vapour deposition:

This method is the most preferred for producing graphene in bulk as it can be used to "fabricate large areas of graphene while limiting the number of defects" [70]. Graphene obtained by using this method can have a carrier mobility that reaches values as high as $\sim 2000-4000$ cm2 V-1 s -1 [71,72,77].

b. Mechanical exfoliation:

"Mechanical exfoliation using the Scotch tape method or by rubbing lithographically patterned pillars with "tipless" atomic force microscopy (AFM) cantilevers can be used to generate very high-quality graphene with a carrier mobility that reaches ~10 000 cm2 V-1 s -1 at room temperature".[71]. "By this method, the physical limit of the graphene sheets that are produced has lateral dimensions on the order of tens to hundreds of micrometers. However, mechanical exfoliation has poor reproducibility and is not amenable to large-scale fabrication". [102].

c. Thermal reduction of GO:

The production of graphene sheets using the method of thermal reduction of GO has been proven to be economical for the production of small graphene sheets but the electrical conductivity of the rGO sheets is lower than those produced by the CVD and mechanical exfoliation processes [73]. Both the quality of graphene and quantity required are essential for the choice of production process as shown in the figure 23.



Fig 20: Methods for the mass-production of graphene [74]

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4.1.2 Applications:

a. Biosensors and chemical sensors

Graphene when used in the structural architecture of devices exhibits low electrical noise. These electrical properties can be enhanced by the absorption of the analyte on its surface which in turn improves the sensitivity levels of the sensors. [75,76]

b. Graphene Transistors (GFET)

FETs using graphene as an active channel layer has shown excellent electrical and mechanical characteristics. Since most semiconductor transistor devices are 3-dimensional, surface electric charges do not always penetrate deep into the device which significantly reduces the device sensitivity. [77] Against this, graphene-based FETs are one carbon thick and thus do directly exposes the channel to any molecule present in its environment. The scientists from the Manchester Centre for Mesoscience and Nanotechnology further proved that "µm-sized sensors fabricated from graphene were capable of detecting individual gas molecules attached or detached from the surface." "Graphene based FETs have a higher carrier mobility which often reach levels greater than 100,000 cm²V ¹s⁻¹ for hBN-encapsulated single-crystal CVD graphene. These devices also have a residual charge carrier density of the order of 5 x 10¹¹ cm⁻²" [90]. The extrinsic scattering that occurs due to charged impurities at the interface of graphene/dielectric result in degradation of the mobility of charge carriers as reported by Chen et al. [100] Lowering the density and effect of charged impurities is critical to improve the mobility. Hence, dielectric material for both substrate and top gate are replaced with high-k materials which reduces the charged impurity scattering because of increased screening effect. Graphene's natural state does not have a band gap & cannot completely block current in the transistor's off-state. Band gap is induced by limiting one dimension and making nanoribbons leading to a larger on-off ratio as reported by Han et al and Li et al. [98,99]. As per the experimental results demonstrated by Lee et al., "in a high performance, graphene based thin film transistor (TFT) on a plastic substrate with graphene active layer, electrode, and ion gel gate dielectrics, this single layer graphene FET provided hole mobility of 26cm²/V. s and electron mobility of 20cm²/V. s at -0.1V of drain bias." But this device showed lower carrier mobility than other graphene-based FETs due to moisture and other chemical species from the porous structure of the PDMS accumulated in the graphene channel. This structure was experimented upon solely due its flexibility and stretchability.

c. Photovoltaic devices

In photovoltaic devices, graphene sheets have been mostly used as charge acceptors [78], transparent electrodes [79, 80], and charge transport bridges, for example, using graphene as a novel charge acceptor, Guo et al. [81] developed quantum dots solar cells with the multilayered graphene-quantum dots device structure and energy level alignment. The resulting device exhibited an incident power conversion efficiency 16% and a photoresponse 10.8 A/m2 under 1000 W/m2 illumination, a milestone in high performance QDSC development.

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d. DSSC (Dye sensitized Solar Cell)

Yang et al., [82] reported graphene fabricated as 2D bridges in the electrodes of DSSCs. These bridges lead to a "fast electron transport, low recombination rate, and a high light scattering". When compared with the nanocrystalline titanium dioxide photoanode, it was found to have shown a total conversion efficiency of 6.97%, an increase of 39%. These were also found to be much better than the 1D nanomaterial composite electrode.

As per Kim et al., Graphene/TiO2 can be used in both DSSC and quantum dots solar cells (QDSC). In the future, due to their high photon-electron efficiency & cost effectiveness, DSSC could be the alternative to traditional silicon solar cells. [83][84]. Tang et al [85] has reported "rGO/TiO2 indiun tin oxides as photoanode for DSSC". The increase of electron transport results in an increase of the short-circuit current density which eventually results in a fivefold increase in power conversion as when compared to pure TiO2.

Optoelectronic devices

An ideal material for usage in optoelectronic applications must be able to transmit more than 90% of light and also offer electrically conductive properties exceeding $1x10^6$ Ω m. Graphene is almost transparent and optically transmits upto 97.7% of light. Its highly conductive, tensile and flexible nature makes it a great suitable material for the future of optoelectronics.

f. **Supercapacitors**

Graphene based supercapacitor materials have significant industrial application as reported by Wang et al [86]. A list of such materials is shown at Fig 24.

Material	Specific Capacitance (F/g)	Energy density Power density
Chemically reduced GO	135	
Graphene-hydrous RuO ₂ (38.3 wt%)	99 570	20.1 W h/kg at 100 mA/g 10 kW/kg at 4.3 W h/kg
Graphene (2675 m ² /g)	550	
Graphene (microwave assisted reduction of GO)	191	At 150 mA/ g discharge current
Thermal reduction in propylene carbonate	120	
Graphene (hydrazine reduced GO)	205	28 W h/kg 10 kW h/g
Graphene-PANI (50:50 wt%) (microwave-solvothermal)	408	
Graphene-PANI (in situ anodic electro-polymerization of PANI film on graphene paper)	233	
Graphene-PANI	1046	
Graphene-MnO ₂	216	
Polymer modified graphene sheet + MWCNT	120	
Curved Graphene Sheets	100-250 at current	85.6 W h/kg at 1 A/g
	density 1 A/g	- 10
Thermally reduced graphene	75 F/g	31.9 W h/kg
	117 F/g	-
MnO ₂ nanowire-graphene	31 F/g	30.4 W h/kg

Fig 21: Graphene based supercapacitor materials [86]

V.DIMENTIONAL

Bulk nanomaterials or 3D nanomaterials are materials that are not confined to the nanoscale in any dimension. They are

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characterized by having dimensions above 100nm. These non-crystalline structure. materials possess a nanomaterials can contain dispersion of nanoparticles, bundles of nanowires and nanotubes as well as multinanolayers. In these, all dimensions are in macroscale.

VI.CONCLUSION

Even though photovoltaic devices have a very high theoretical energy conversion limit i.e above 33% for single junctions and ultimately close to 90% if suitable materials are found, practically only about a maximum of 13.4% efficiency has been reported till date. Life cycle constraints (toxicity, recyclability, etc) are a major issue and will become more prevalent as the productions reach volumes near the terawatt scale. Increasing number of researchers are engaged towards addressing this issue. One of the significant advantages of QDs in solar cells is their capability of capturing the untapped energy of UV and IR wavelengths of the solar radiation as well and extensive research is ongoing to increase the practical efficiency limits and to be able to maximise the absorption of energy in these spectra. The carbon nanotube transistor certainly offers the possibility of replacing the silicon transistor in future. [49]. Researchers have already established that it would meet the low power and high current density performance parameters and also the scaling demands as per Moore's law and Dennard's transistor scaling for use in future Digital Logic Circuits and memory chips (SRAMs).[55]. However, the current challenge is the industrial production of the chips with CNTFET with channel lengths of less than 10 nm without losing out on the channel array precision and purity levels. The continued efforts of IBM have resulted in a chipset with 10,000 nanotube transistors but this has to reach a billion transistors to make it viable. The breakthrough in the fabrication process as reported by the engineers at Wisconsin University and works of other researchers holds the key. All this needs to be achieved as per the targeted time frame for industrial production around 2020 to meet the scalability challenge facing the silicon chips. The GFETs is poised slightly behind the CNTFET in terms of the timelines for their industrial production. While Graphene does not have the constraint of dimensional / orientation impact as in the case of CNT it surely shares similar challenges in terms of purity of the Graphene material for use as FET channel. Graphene application as 2D bridges in Dye Sensitised Solar Cells and as sensors for gas molecule detection holds significant industrial application in future. The paper has endeavoured to highlight the facets of research on nanomaterials and its influence on the electronic components and devices in the future and hence will redeem itself as the 'toolkit for future electronics'.

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