

Performance of Interline Unified Power Quality Conditioner (IUPQC) With PI, Fuzzy and ANFIS Controllers



A Navya, A Panduranga Rao, L Shanmukha Rao

Abstract: Several artificial intelligent control schemes are highly used in several applications, in that ANFIS controller has been greatly recognized due to enhanced performance over the classical PI and Fuzzy controllers. At present the multi-feeder power distribution system is deteriorated with continuity of supply and poor power quality standards. In this multi-feeder distribution system, it is a regular consumer related issue which is acquired due to malfunctioning of massive non-linear loads. These loads create the voltage or current imperfections on distribution networks which disrupts the power quality of distribution system. An efficient and reliable active compensation scheme is used for attaining enhanced power quality features at PCC of multi-feeder distribution system with effective control functions. The Multi-Feeder Unified Power Quality Compensator (MF-UPQC) is optimal choice for attaining enhanced power quality features and it is a combined shunt or series compensator driven by common DC-link. This paper recommends the Adaptive Neuro-Fuzzy Intelligent Controller (ANFIS) based prediction technique for generation of optimal switching states to enhance performance of proposed MF-UPQC to compensate all voltage-current PQ imperfections. The performance of proposed MF-UPQC is verified by classical PI, Fuzzy and proposed ANFIS control functions by using MATLAB/SIMULINK tool and results are conferred with proper comparisons.

Keywords: Adaptive Neuro-Fuzzy Inference System, Hybrid-Fuzzy Logic Controller, Multi-Feeder Distribution System, Fuzzy-Logic Controller, Power Quality Improvement; PI Controller, Total Harmonic Distortion (THD).

I. INTRODUCTION

Economic growth of any nation depends upon the reliable and quality of Electrical power availability to industries and agricultural sector. With growing industries and agriculture demand of electric power utility are striving hard to maintain greater power-quality standards in electric power supply [1]. In addition, the increased utilization of power electronics devices in electric utility system causes poor power quality and affects the Electric Power Systems.

Obviously, it is deteriorated from the voltage/current power quality (PQ) imperfections, which includes voltage and/or current harmonic distortions, reactive power mitigation, power factor improvement, voltage-sag, voltage swell, un-balancing loading. These PQ issues in multi-feeder distribution systems are not a new things but the awareness of these PQ issues has been increased at current situation by end-user consumers [2].

Traditional PQ compensation schemes like passive filters, static VAR capacitors, etc., have several demerits such as fixed compensation, massive, low dynamic response, resonance issues with source impedances, etc. An advanced Custom-Power (CP) apparatus based Active compensation methodology is used for improvement of above-specified PQ issues. The CP technology plays a significant role in power distribution system comprising of active power semiconductor technology to enhance the PQ features with a reliable operation and maintain the system as balanced way [3]. Several CP techniques use Voltage-Source Inverter (VSI) with an attractive control functions with the help of optimal reference voltage or current generation schemes. The unique dual-VSI based Unified Power Quality Compensator (UPQC) device is highly suitable for three phase distribution system, but it is un-feasible for multi-feeder system [4]. Alternatively, the choice is multi-feeder custom power apparatus can be integrated in between two adjacent lines for compensating current-voltage imperfections. The multi-feeder CP apparatus utilizes dual and/or more VSI's which are associated back-to-back formed as shunt-shunt, series-shunt and series-series modules. The merits of multi-feeder compensation devices are, enhances power-quality features occur in single and/or multi-feeders, another feeder provides energy to enhance the PQ concerns. Thus, the multi-feeder power-quality enhancement devices satisfy the unique performance over the single-feeder compensation devices. The multi-feeder UPQC (MF-UPQC) scheme furnishes the superior enhanced all voltage-current related PQ features in power distribution systems by using attractive control strategies. Various control strategies are used in MF-UPQC such as, Instantaneous PQ theory [5] is used for generation of reference current signal to shunt-VSI in Feeder-I and Synchronous Reference Frame (SRF) theory [6] is used for generation of reference voltage signal to series-VSI in Feeder-II with the help of common DC-link controller.

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In general, DC-link controller utilizes the Proportional-Integral (PI) controller and the performance of controller depends on gain factors. The gain factors are initiated as trial and error process which is experienced based on control functions for acquiring the efficient performance to maintain DC-link voltage as constant. But, this classical PI and Fuzzy controller never regulates the DC-link voltage under sudden interruptions, parameter variations, moderate PQ compensation characteristics etc. This paper proposes novel Adaptive Neuro-Fuzzy Inference System (ANFIS) control function with good improved characteristics. It is constituted by emblematic path along with knowledge proficiency for prediction of optimal switching states to proposed MF-UPQC to acquire improved PQ features in a multi-feeder distribution system. The main intension of the ANFIS controller is to regulate the improved PQ features over the formal PI and fuzzy controllers. The performance of MF-UPQC is evaluated under the presence of classical PI, Fuzzy/Hybrid Fuzzy Logic and ANFIS controllers by using MATLAB/SIMULINK tool, simulation results conferred with comparative studies.

II. PROPOSED MULTI-FEEDER UPQC DEVICE

The schematic diagram of proposed MF-UPQC is shown in Fig.1, which consists of three-phase dual-VSI topologies as shunt-VSI and series-VSI integrated with a common DC-link capacitor. The source of dual feeder considered as 415V driving the non-linear and sensitive loads in feeder-1 and feeder-2, respectively. The LC filter units are interfaced to minimize the switching ripples generated by VSI which is controlled by proposed ANFIS controller. The feeder-1 is affected due to current harmonic distortions coming from non-linear load and feeder-2 which is affected due to LLL-G fault conditions, because these the PCC of multi-feeder distribution system is affected and attains poor PQ features. These PQ features are enhanced by initializing the MF-UPQC device with attractive control functions.

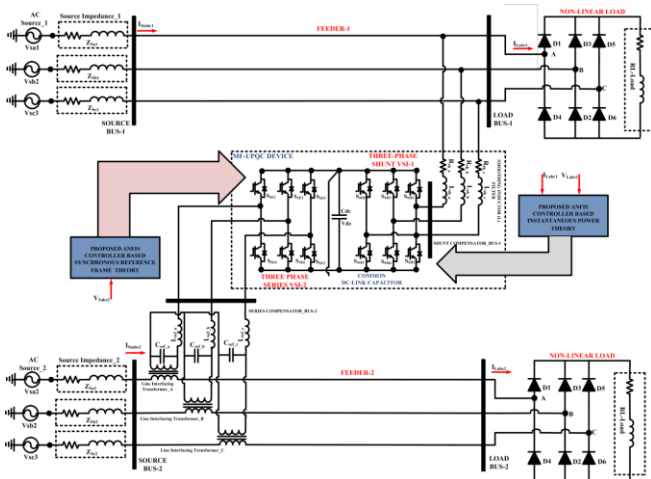


Fig.1 Schematic Diagram of Proposed MF-UPQC device with Proposed ANFIS Control Based Theory

III. PROPOSED CONTROL FUNCTIONS

The control scheme of proposed MF-UPQC device comprises of shunt-VSI and series-VSI compensator. An attractive and simple control algorithm was implemented to

generate the reference current or reference voltage to achieve the optimal switching states to both shunt and series compensators. The control scheme of series-VSI utilizes SRF theory for generation of reference voltage signals, the SRF theory is commonly used in MF-UPQC device [7], [8]. The shunt-VSI of MF-UPQC utilized IRP theory for generation of reference current signal, control the real and reactive power and attains high dynamic response which effects the entire system.

A. Control Scheme of Shunt-VSI Compensator

The Instantaneous Real-Reactive power theory is very attractive control theory for generation of reference current signal with the controllability of real and reactive powers. The operating principle of IRP theory is generally situated based on transformation process of three-phase abc quantities to two quantities as $\alpha\beta$ sequences in a coordinated orthogonal frame. The input variables of IRP theory is load currents (I_{Labc}) from sensors and source voltage (V_{sabc}) are fed to Park's transformation process [9]. This scheme provides the current-voltage component in coordinated orthogonal ($V_{\alpha\beta}, I_{\alpha\beta}$) are used to represent the real and reactive powers. The instantaneous real & reactive power sequences are measured based on above transformation process by specific equations as fundamental active current sequence and voltage functions ($V_{s\alpha\beta}, I_{L\alpha\beta}$). The instantaneous vector coordinates are V_{sa}, i_{La} posed on the axis-"a" and respective magnitudes are changed with positive-negative with time. By using Park's transformation process these phases are transforming to ($\alpha\beta$) coordinates, follows as

$$\begin{bmatrix} v_{s\alpha} \\ v_{s\beta} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} v_{sa} \\ v_{sb} \\ v_{sc} \end{bmatrix} \quad (1)$$

$$\begin{bmatrix} i_{L\alpha} \\ i_{L\beta} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} i_{sa} \\ i_{sb} \\ i_{sc} \end{bmatrix} \quad (2)$$

Where, the ($\alpha\beta$) are coordinated orthogonal sequences, the classical immediate power for system can be depicted as;

$$p = v_{sa} i_{La} + v_{sb} i_{Lb} \quad (3)$$

The formal active power equation is defined as;

$$p = v_{sa} i_{sa} + v_{sb} i_{sb} + v_{sc} i_{sc} \quad (4)$$

Relatively, the formal IRP theory is defined as;

$$q = -v_{sb} i_{La} + v_{sa} i_{Lb} \quad (5)$$

Although, the instantaneous real-reactive power is illustrated in matrix form as;

$$\begin{bmatrix} p \\ q \end{bmatrix} = \begin{bmatrix} v_{sa} & v_{s\beta} \\ -v_{s\beta} & v_{sa} \end{bmatrix} \begin{bmatrix} i_{L\alpha} \\ i_{L\beta} \end{bmatrix} \quad (6)$$

The $(\alpha\text{-}\beta)$ current components can be acquired as;

$$\begin{bmatrix} i_{L\alpha} \\ i_{L\beta} \end{bmatrix} = \frac{1}{\Delta_k} \begin{bmatrix} v_{sa} & v_{s\beta} \\ -v_{s\beta} & v_{sa} \end{bmatrix} \begin{bmatrix} p \\ q \end{bmatrix} \quad (7)$$

Where,

$$\Delta_k = v_{s\alpha}^2 + v_{s\beta}^2 \quad (8)$$

The active-current sequence is by-pass to High-Pass Filter (HPF) which is used to recognizing the harmonic elements from desired wave-shape. This filter eliminates the lower-order frequencies and allows high-order frequency into the system as reference current generation. The DC-link controller is used to regulate the common DC-link voltage of DCMLI-MCUPQC by utilizing ANFIS controller, which is used to control the DC-link voltage as maintained constant.

The reference DC-link voltage (V_{dcr}^*) is directly compared to measured DC-link voltage (V_{dca}) and resembles the error quantities. The outcome error value from this task is fed to propose ANFIS controller for minimization of dominant error quantities in P_{Loss} with good stability index. The instantaneous real (p) and reactive power (q) can be conveyed into a DC average and oscillatory components are,

$$\begin{aligned} p &= \overline{p} + p \\ q &= \overline{q} + q \end{aligned} \quad (9)$$

Where, \bar{p} and \bar{q} is DC average value p and q is AC oscillatory value of instantaneous real-reactive power theory. The reference compensator currents $i_{c\alpha}^*$ and $i_{c\beta}^*$ into $(\alpha - \beta)$ coordinates can be evaluated as

$$\begin{bmatrix} \dot{i}_{c\alpha} \\ \dot{i}_{c\beta} \end{bmatrix} = \frac{1}{\Delta_k} \begin{bmatrix} v_a & -v_\beta \\ v_\beta & v_a \end{bmatrix} \begin{bmatrix} p \\ q \end{bmatrix} \quad (10)$$

These reference currents are transformed to a-b-c components using Inverse transformation process as;

$$\begin{bmatrix} \dot{i}_{ca} \\ \dot{i}_{cb} \\ \dot{i}_{cc} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \frac{1}{\sqrt{2}} & 1 & 0 \\ \frac{1}{\sqrt{2}} & -\frac{1}{2} & \frac{\sqrt{3}}{2} \\ \frac{1}{\sqrt{2}} & \frac{1}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} \dot{i}_0 \\ \dot{i}_{c\alpha} \\ \dot{i}_{c\beta} \end{bmatrix} \quad (11)$$

The evolution of reference current component in orthogonal coordinate (i_{abc}^*) is extracted by summation of active fundamental current component and (Δ_{id}) component is called utmost reference current. The reference current (i_{abc}^*) is differentiated to actual measured current from feeder-1 for producing optimal switching states to shunt-VSI compensator by Hysteresis Current Controller (HCC). The HCC is highly engaged to develop the optimal switching states to shunt VSI of MF-UPQC by using hysteresis band limits. This band limit acts as boundary conditions of

compensation current which is controlled in between the upper and lower limits to generate the switching pulses to compensator. The switching pulses related to ON/OFF of switches in VSI are greatly depended by reference current and actual current component. When actual current is increased more than reference current then the respective switch is conducted and decreased the switch which is in OFF condition. However, the actual current is continuously swinging between inside the bands limits followed by reference current component provided by I_d - I_q synchronous detection scheme. The control objective of shunt-VSI of MF-UPQC device is depicted in Fig.2.

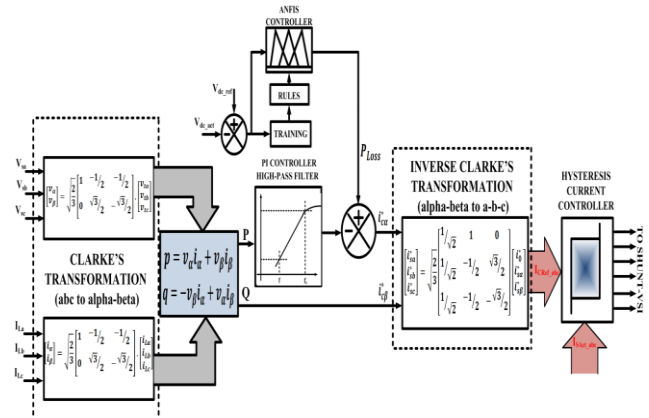


Fig.2 Schematic Diagram of Shunt-VSI Control Scheme of MF-UPOC Device

B. Control Scheme of Series-VSI Compensator

A well-recognized control scheme is best selection for superior performance of series-VSI compensator for MF-UPQC. The main intension of series-VSI controller is to maintain the constant voltage magnitude at Feeder-1 and Feeder-2 under voltage-imperfections. The working of series-VSI of MF-UPQC is greatly depends on analogy of appropriate load voltage with respect to actual line voltage [10]. The attained error quantities are resolved dynamically by differentiating the actual and reference voltage values. The Phase-Locked Loop (PLL) is used to generate the unit-sinusoidal functions which are in-phase with main utility-grid voltage. The actual voltage measured from load-bus of Feeder-2 (V_{Labc}) are transformed into dq quantities as V_{dq0act} by using Clarke's transformation process.

$$\begin{bmatrix} V_{dact} \\ V_{qact} \\ V_0 \end{bmatrix} = \frac{2}{3} \begin{bmatrix} \cos \theta & \cos[\theta - \frac{2\pi}{3}] & \cos[\theta + \frac{2\pi}{3}] \\ \sin \theta & \sin[\theta - \frac{2\pi}{3}] & \sin[\theta + \frac{2\pi}{3}] \\ \frac{1}{2} & \frac{1}{2} & \frac{1}{2} \end{bmatrix} \begin{bmatrix} V_{La} \\ V_{Lb} \\ V_{Lc} \end{bmatrix} \quad (12)$$

The “ ω ” angular velocity and θ is the displacement factor which is associated by

$$\theta = \int \omega dt \quad (13)$$

The control scheme of series-VSI of MF-UPQC encounters the abc to dq transformation of reference and actual voltages. During the normal and symmetrical situations, the voltage is sustained constant and the d -frame voltage is retained as unity (p.u) and q -frame voltage which set as zero (0 p.u). After the transformation process, the outcome voltage like (V_{dqact}) is compared to reference voltages (V_{dqref}). When both the actual and reference voltage vectors are compared and attain some error quantities. These error quantities are minimized by using proposed ANFIS controller.

Ultimately, the error is counteracted and the outcome values treated as reference voltage generation in $d-q$ frame is (V_{dq}^*).

The reference voltage values in $d-q$ frame is re-transformed to abc frame by using inverse-transformation process as shown in Equation (15).

$$V_{dq}^* = V_{dqref} - V_{dqact} \quad (14)$$

$$\begin{bmatrix} V_{aref} \\ V_{bref} \\ V_{cref} \end{bmatrix} = \frac{2}{3} \begin{bmatrix} \cos \theta & \sin \theta & 1 \\ \cos[\theta - \frac{2\pi}{3}] & \sin[\theta - \frac{2\pi}{3}] & 1 \\ \cos[\theta + \frac{2\pi}{3}] & \sin[\theta + \frac{2\pi}{3}] & 1 \end{bmatrix} \begin{bmatrix} V_d^* \\ V_q^* \end{bmatrix} \quad (15)$$

The reference voltage in abc frame is V_{abcref} is used as reference voltage signal to generate optimal switching states by using sinusoidal PWM scheme. The schematic diagram of series-VSI control scheme of MF-UPQC is shown in Fig.3.

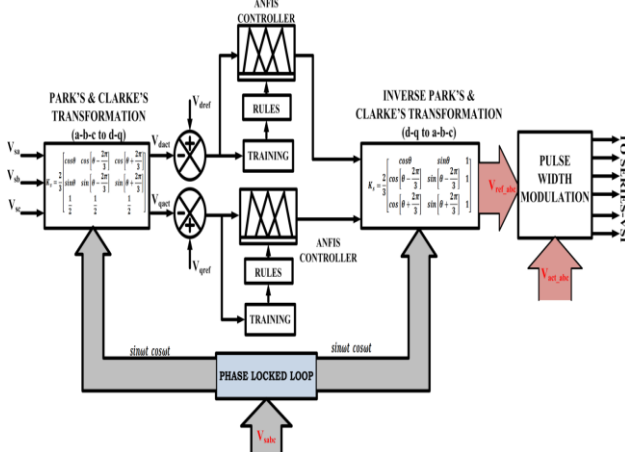


Fig.3 Schematic Diagram of Series-VSI Control Scheme of MF-UPQC Device

C.Proposed ANFIS Controller

Several artificial intelligent control schemes are highly used in several applications, In that ANFIS controller has been greatly recognized due to enhanced performance over the classical PI and Fuzzy controllers [11], [12]. The eminent characteristics of intelligent controller is that they comprising as symbolic notation of inference system along with expertise knowledge. It is more requisite for optimal performance of MF-UPQC; the best suitable controller for this application is

ANFIS which is predominantly enhances the PQ features. It is the unique integral approach for respective task and yields the outcome switching sequences to the VSI of MF-UPQC. In ANFIS, the knowledge system is acquired from supervised learning algorithm by utilizing the hybrid method or back-propagation algorithm [13]. It can improve the over-all system performance in many ways such as good stability index, better reduction of THDs, smooth operation, more reliability, etc. It has incredible ability to attain a supported group as an optimal membership functions are highly used for clustering and deducting the acquired outcome within nominal epochs. The principle of ANFIS structure at initial state is; system provides the test data fuzzy rules which are transformed to attractive training methodology for generation of optimum membership functions and rules. It any violations

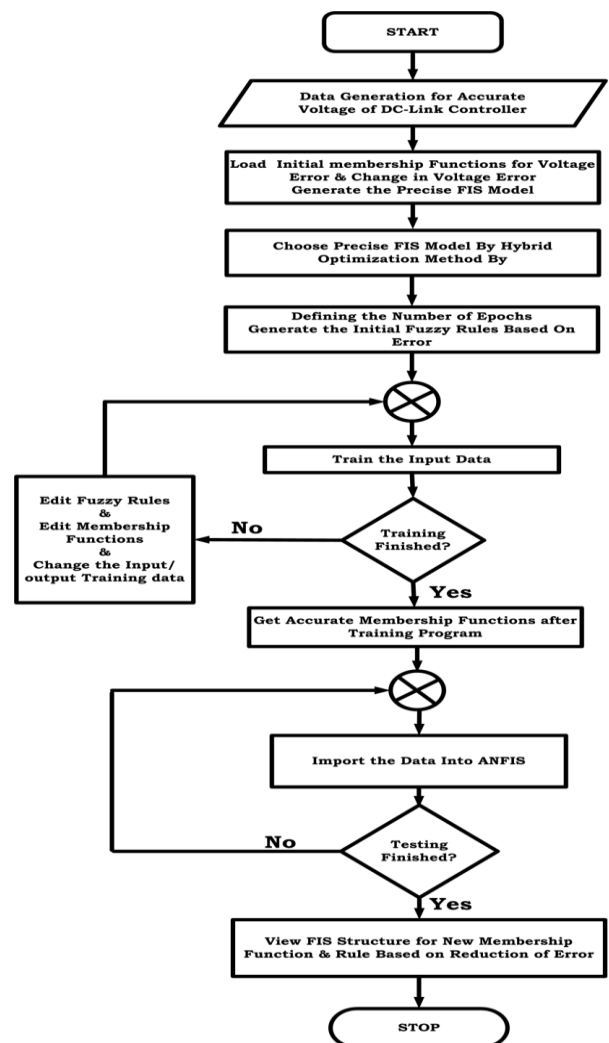


Fig.5. Flowchart of ANFIS Controller

the predefined error value, the second process is initiated. It is nothing but, based on provided rules/membership functions, ANFIS generates the new FIS configuration with respect to system error and change in error quantities. The membership functions for error (E) and change in error (CE) are depicted in Figure.4.

$$e(S) = \omega_r^* - \omega_{act} \quad (16)$$

$$\Delta e(S) = e(S) - e(S-1) \quad (17)$$

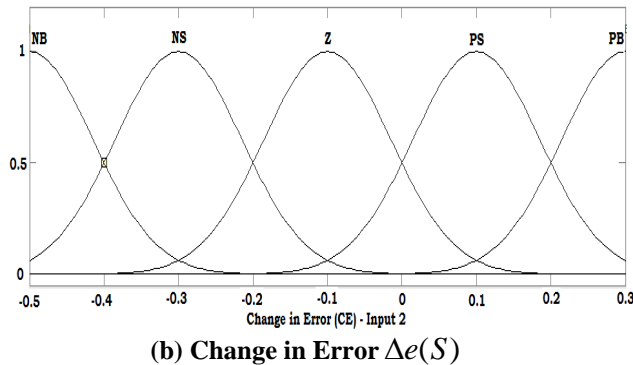
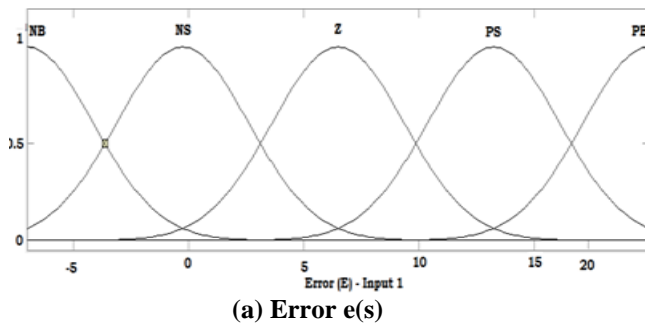


Figure.4. Membership Functions

Several membership functions are EZ-Zero Error; PS-Positive Small; PB-Positive Big and NS-Negative Small and NB-Negative Big value respectively. Several rules of the ANFIS structure are illustrated as below in Table.1. The flowchart of the ANFIS Controller is illustrated in Figure.5.

TABLE.1. RULES FOR ANFIS STRUCTURE

$e(S) / \Delta e(S)$	NB	NS	EZ	PS	PB
NB	R1	R2	R3	R4	R5
NS	R6	R7	R8	R9	R10
Z	R11	R12	R13	R14	R15
PS	R16	R17	R18	R19	R20
PB	R21	R22	R23	R24	R25

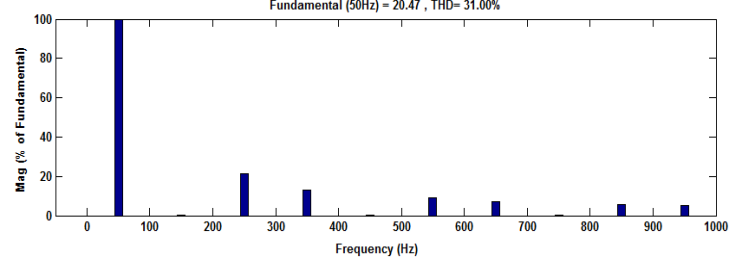
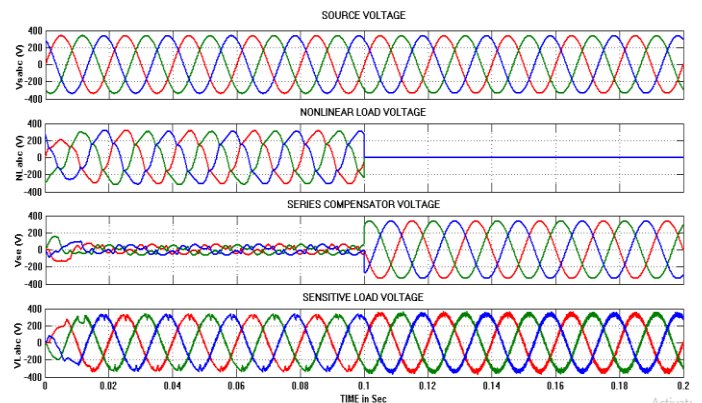
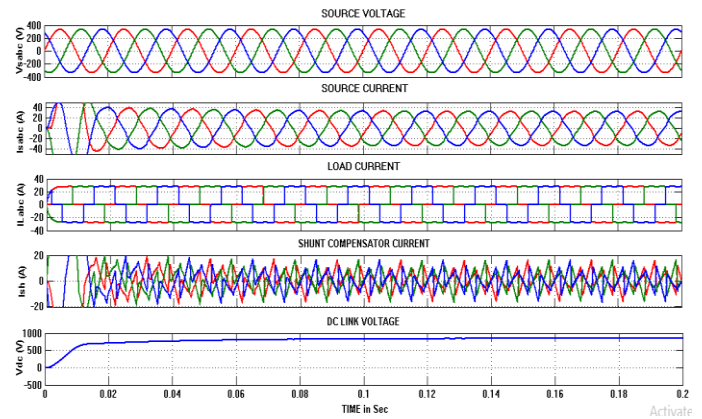
IV. MATLAB/SIMULINK RESULTS

The performance of proposed MF-UPQC is verified under various control functions by using MATLAB/SIMULINK tool with operating specifications as depicted in Table.2.

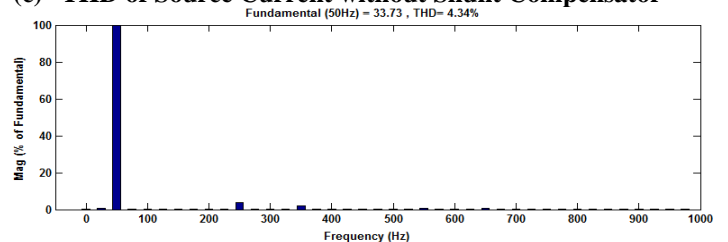
Table.2 Operational Parameters of Proposed MF-UPQC System in Multi-Feeder Distribution System

S. No	Several Operational Parameters	Values
1	Feeder-1 and Feeder-2 Source Voltage	415 V, 50Hz,
2	Source Impedance	$R_s=0.15\Omega$, $L_s=0.9mH$
3	Load Impedance	$R_L=30\Omega$, $L_L=20mH$
4	Line Interfacing Transformer (1:1)	415V, 50Hz, 5KVA, Linear Model, 10% Leakage Reactance
5	Series-VSI Filter Module	$L_{sc}=3mH$, $C_{sc}=100\mu F$
6	Shunt-VSI Filter Module	$R_{sh}=0.001\Omega$, $L_{sh}=10mH$
7	DC Link Source (Capacitor)	$V_{dc}=880V$, $C_{dc}=1500\mu F$

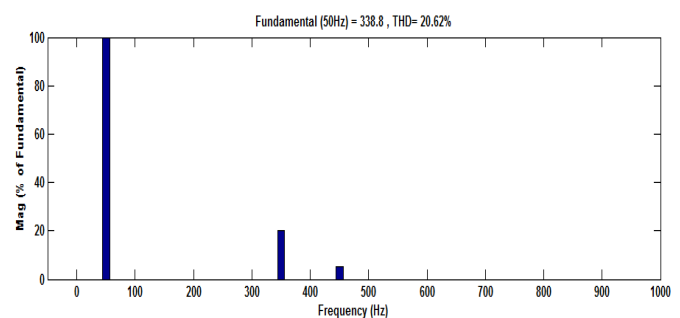
Case A: Performance of Proposed MF-UPQC device under Classical PI Controller



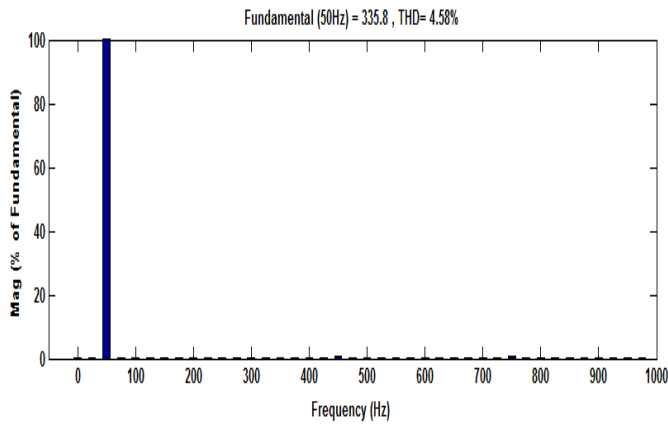
(c) THD of Source Current without Shunt Compensator



(d) THD of Source Current



(e) THD of Load Voltage without Series Compensator

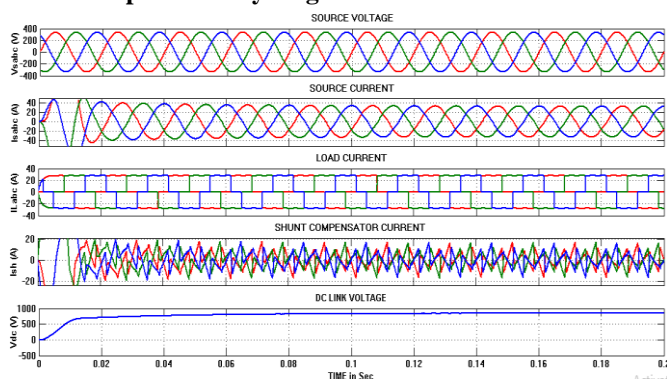


(f) THD of Load Voltage

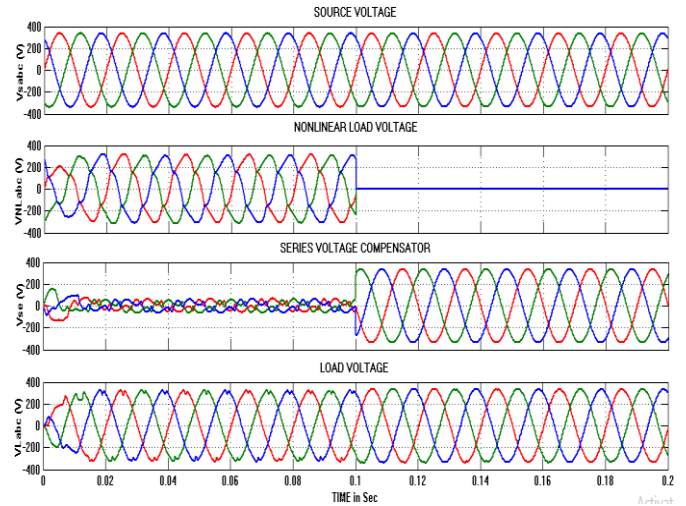
Fig.6 Simulation Results of Proposed MF-UPQC under Classical PI Controller in Multi-Feeder Distribution System

The performance of proposed MF-UPQC under classical PI controller in Multi-Feeder distribution system is shown in Fig.6. In that, Fig. 6(a) represents the shunt-VSI characteristics, the source voltage, source current, load current, compensation current and DC-link voltage are the key parameters. The shunt-VSI acts as controlled current-source to compensate current harmonics coming from non-linear load and maintain PCC current as balanced and sinusoidal nature and the DC-link voltage maintained as constant. Fig. 6(b) represents the series-VSI characteristics, the source voltage, non-linear load voltage, sensitive load voltage and compensator voltage which are the key parameters. The series-VSI acts as controlled voltage-source and injects pre-requisite voltage to compensate voltage-sag due to LLL-G fault and load voltage as balanced and maintained as constant. The Total Harmonic Distortions (THD) of source current without shunt compensator is 31.00% as depicted in Fig.6 (c) and the THD of source current with shunt-VSI is 4.34% as depicted in Fig.6 (d) and the THD of load voltage without series compensator is 20.62% as depicted in Fig.6 (d) with series-VSI is 4.58% as depicted in Fig.6 (e).

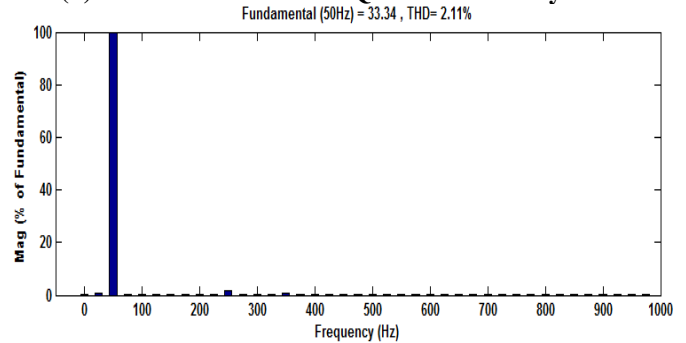
Case B: Performance of Proposed MF-UPQC device under Proposed Fuzzy-Logic Controller



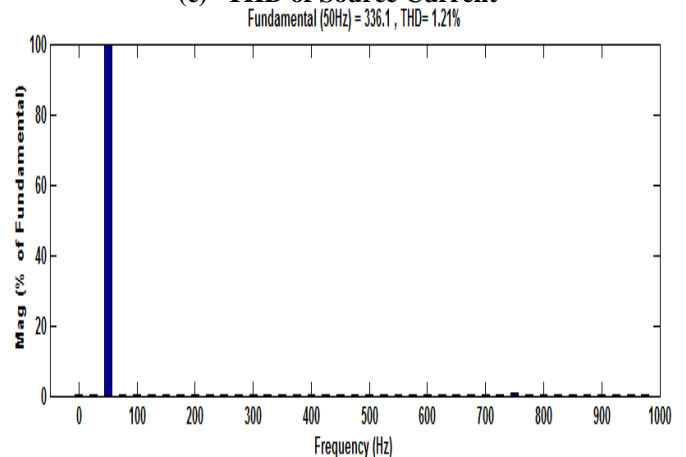
(a) Shunt-VSI of MF-UPQC in Feeder-1 System



(b) Series-VSI of MF-UPQC in Feeder-2 System



(c) THD of Source Current



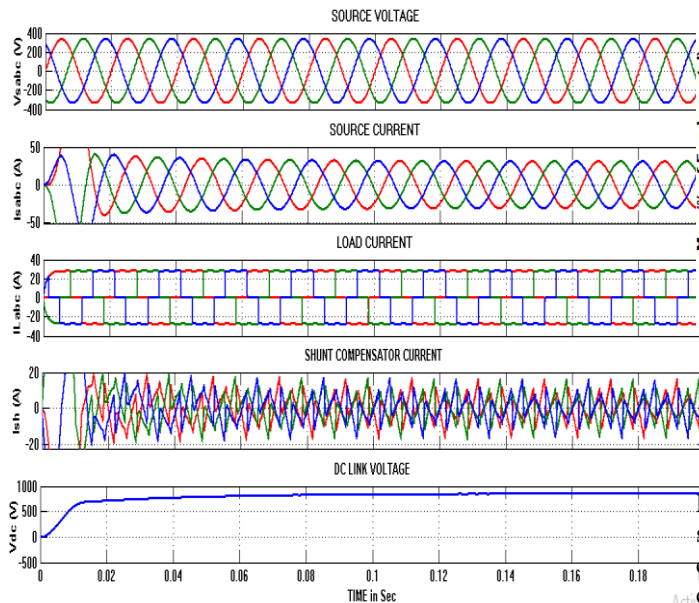
(d) THD of Load Voltage

Fig.7 Simulation Results of Proposed MF-UPQC under Proposed Fuzzy-Logic Controller in Multi-Feeder Distribution System

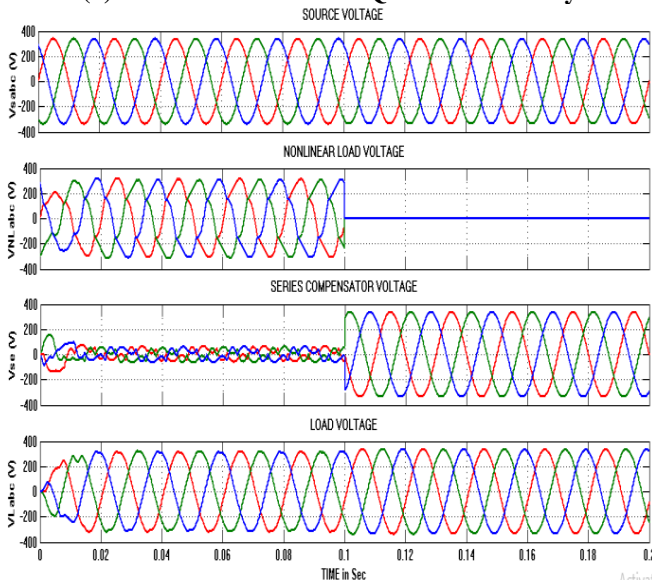
The performance of proposed MF-UPQC under Proposed Fuzzy-Logic controller in Multi-Feeder distribution system is shown in Fig.7. In that, Fig. 7(a) represents the improved shunt-VSI characteristics over the classical PI controller, the source voltage, source current, load current, compensation current and DC-link voltage which are the key parameters. The shunt-VSI acts as controlled current-source to compensate current harmonics coming from non-linear load and maintain PCC current as balanced and sinusoidal nature with enhanced PQ features and the DC-link voltage maintained as constant.

Fig. 7(b) represents the series-VSI characteristics over the classical PI controller, the source voltage, non-linear load voltage, sensitive load voltage, compensator voltage are the key parameters. The series-VSI acts as controlled voltage-source and injects pre-requisite voltage to compensate voltage-sag due to LLL-G fault and load voltage as balanced and maintained as constant. The Total Harmonic Distortions (THD) of source current with shunt-VSI is 2.11% as depicted in Fig.7 (c) and the THD of load voltage with series-VSI is 1.21% as depicted in Fig.7 (d). The obtained THD values are within IEEE-519 standard limitations.

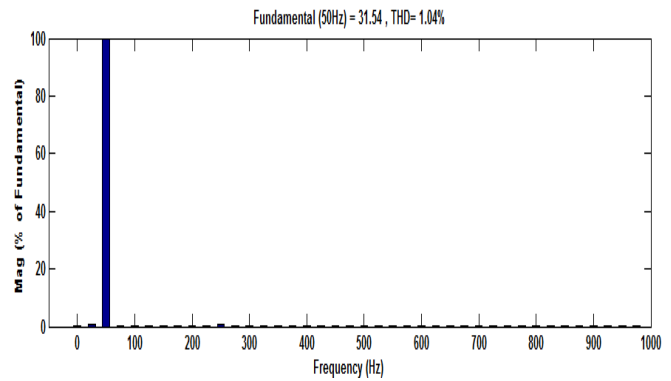
Case C: Performance of Proposed MF-UPQC device under Hybrid Fuzzy-Logic Controller



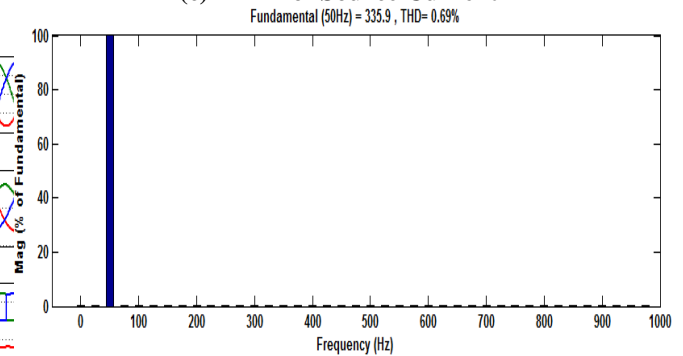
(a) Shunt-VSI of MF-UPQC in Feeder-1 System



(b) Series-VSI of MF-UPQC in Feeder-2 System



(c) THD of Source Current

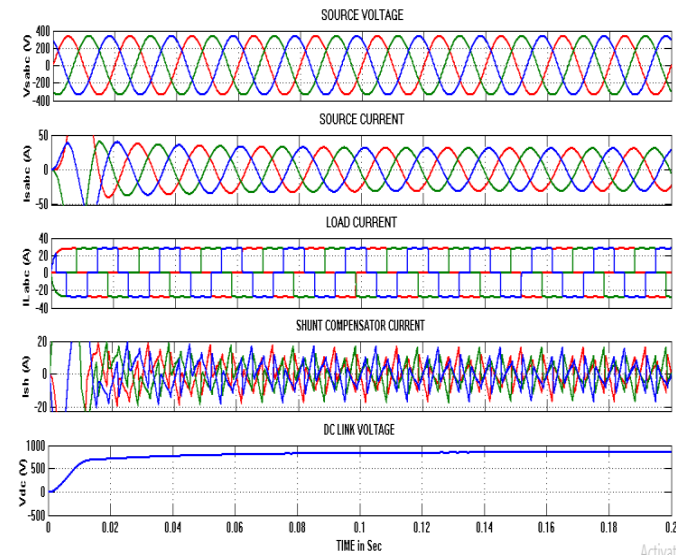


(d) THD of Load Voltage

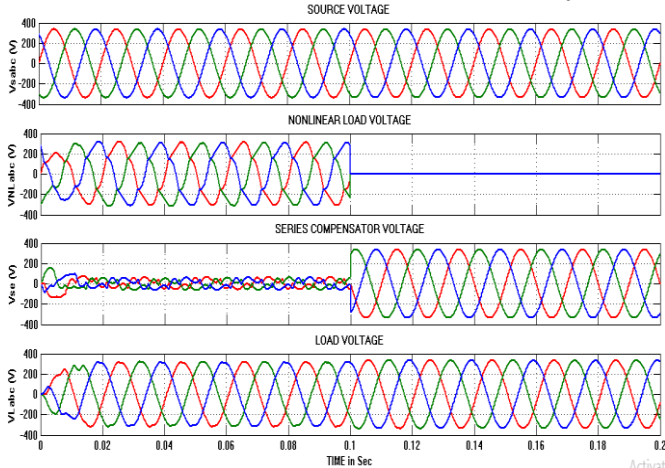
Fig.8 Simulation Results of Proposed MF-UPQC under Hybrid Fuzzy-Logic Controller in Multi-Feeder Distribution System

The performance of proposed MF-UPQC under Hybrid Fuzzy-Logic controller in Multi-Feeder distribution system is shown in Fig.8. Fig. 8(a) represents the improved shunt-VSI characteristics over the classical PI and Fuzzy-Logic controller, the source voltage, source current, load current, compensation current and DC-link voltage are the key parameters. The shunt-VSI acts as controlled current-source to compensate current harmonics coming from non-linear load and maintain PCC current as balanced and sinusoidal nature with enhanced PQ features and the DC-link voltage maintained as constant. Fig.8(b) represents the series-VSI characteristics over the classical PI and Fuzzy-Logic controller, the source voltage, non-linear load voltage, sensitive load voltage, compensator voltage are the key parameters. The series-VSI acts as controlled voltage-source and injects pre-requisite voltage to compensate voltage-sag due to LLL-G fault and load voltage as balanced and maintained as constant. The Total Harmonic Distortions (THD) of source current with shunt-VSI is 1.04% as depicted in Fig.8 (c) and the THD of load voltage with series-VSI is 0.69% as depicted in Fig.8 (d). The obtained THD values are well within IEEE-519 standard limitations and attain enhanced PQ features over the classical control functions.

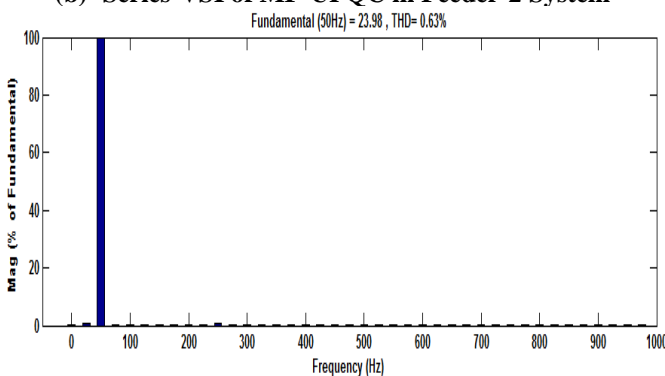
Case D: Performance of Proposed MF-UPQC device under Proposed ANFIS Controller



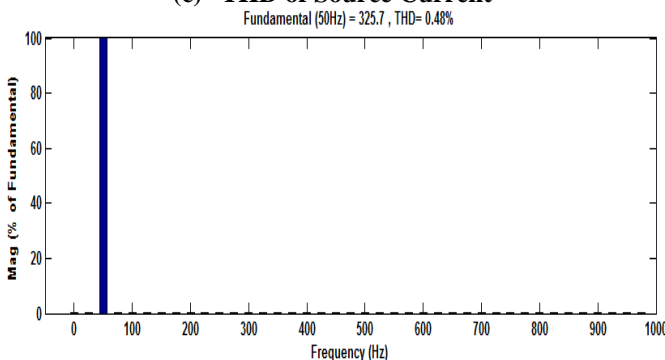
(a) Shunt-VSI of MF-UPQC in Feeder-1 System



(b) Series-VSI of MF-UPQC in Feeder-2 System



(c) THD of Source Current



(d) THD of Load Voltage

Fig.9 Simulation Results of Proposed MF-UPQC under Proposed ANFIS Controller in Multi-Feeder Distribution System

The performance of proposed MF-UPQC under Proposed ANFIS controller in Multi-Feeder distribution system is shown in Fig.9. In that, Fig. 9(a) represents the improved shunt-VSI characteristics over the classical PI, Fuzzy and Hybrid-Fuzzy Logic controller, the source voltage, source current, load current, compensation current and DC-link voltage which are the key parameters. The shunt-VSI acts as controlled current-source to compensate current harmonics coming from non-linear load and maintain PCC current as balanced and sinusoidal nature with enhanced PQ features and the DC-link voltage maintained as constant. Fig.9(b) represents the series-VSI characteristics classical PI, Fuzzy and Hybrid-Fuzzy Logic controller, the source voltage, non-linear load voltage, sensitive load voltage and compensator voltage which are the key parameters. The series-VSI acts as controlled voltage-source and injects pre-requisite voltage to compensate voltage-sag due to LLL-G fault and load voltage as balanced and maintained as constant. The Total Harmonic Distortions (THD) of source current with shunt-VSI is 0.63% as depicted in Fig.9 (c) and the THD of load voltage with series-VSI is 0.48% as depicted in Fig.9(d). The obtained THD values are well within IEEE-519 standard limitations and attain enhanced PQ features over the classical control functions. Over the classical PI, Fuzzy and Hybrid-Fuzzy Logic controller, the proposed ANFIS controller has better reduction of source current and load voltage distortions. The THDs of both parameters are well within IEEE standard limitations for attaining improved PQ features in a multi-feeder distribution system as depicted in Table.3 and the graphical view is depicted in Fig.10.

Table.3 THD Comparison of Source Current and Load Voltage under Several Control Functions

THD (%)	With out Compensator	Classical PI Controller	Classical Fuzzy Logic Controller	Classical Hybrid Fuzzy Logic Controller	Proposed ANFIS Controller
Source Current	31.00%	4.34%	2.11%	1.04%	0.63%
Load Voltage	20.62%	4.58%	1.21%	0.69%	0.48%

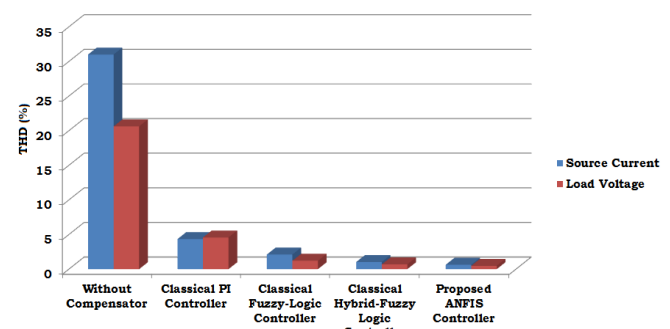


Fig.9 Graphical View of THD Analysis under Various Control Functions

V. CONCLUSION

The performance of proposed MF-UPQC is verified under non-linear loads and LLLG-fault conditions; the proposed dual compensation technology. It is effectively used to compensate the all current-voltage related PQ issues in multi-feeder distribution. The proposed MF-UPQC is evaluated by using MATLAB/SIMULINK tool under various control functions for attaining improved PQ features. Over the classical PI controller, Fuzzy-Logic controller, and Hybrid Fuzzy-Logic controller, the proposed ANFIS controller has good compensation characteristics and the THD values are well within IEEE-519 standards.

From above discussions the MF-UPQC with proposed ANFIS controller furnishes best performance for all voltage-current imperfections.

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